FULLY ADAPTABLE BAND-STOP FILTER USING VARACTOR DIODES

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ABSTRACT: In this article a reconfigurable band-stop filter able to reconfigure center frequency, bandwidth, and selectivity for fine tuning applications is demonstrated, device topology discussion and implementation details are given, and followed by discussion on simulations and measurements. The reconfigurable filter topology has four poles and a quasi-elliptic band-stop filter response. The device is tuned by varactor diodes placed at different locations on the filter; varactors are voltage controlled in pairs due to filter symmetry for center frequency and bandwidth control. An additional varactor is placed on a crossing line to move a pair of transmission zeros, closer or farther to the filter center frequency, which tunes filter selectivity. Simulations show a tuneable center frequency range from 1.42 to 1.48 GHz; a tuneable fractional bandwidth range from 9.46 to 12.96%, and a tuneable selectivity range from 0.53 to 0.65 dB/MHz. Measurements show a tuneable center frequency range from 1.37 to 1.43 GHz; a tuneable fractional bandwidth range from 11.31 to 15.94%, and a selectivity tuning range from 0.37 to 0.40 dB/MHz. Simulations and measurements are in good agreement.


Key words: continuously tuned filter; band-stop quasi-elliptic filter; varactor diode; reconfigurable filter; transmission zeros

1. INTRODUCTION

There is an increased demand for microwave filters with advanced features that can make radio frequency (RF) systems much more efficient and adaptable to multiple bands. Reconfigurable filters can reduce the complexity of a system avoiding the introduction of filter banks; the filter presented in this article finds its application in adaptable image rejection receivers. Earlier work has been primarily focused on center frequency or bandwidth control [1–8]. Filter parameters like center frequency or bandwidth can be controlled continuously, discretely, or a combination of both. Continuously tuned filters have been implemented using varactor diodes [9–15], MEMS varactors [16–20], or ferroelectric materials [21–24]. On the other hand, discretely tuned filters have been implemented using PIN diodes [25–31] or MEMS switches [32–34]. The band-stop filter in Ref. [35] uses PIN diodes to control filter center frequency discretely, meanwhile varactor diodes provide a continuous bandwidth reconfiguration at a given frequency. The filter presents discrete center frequencies in the range from 0.5 to 2 GHz, and continuous bandwidth reconfiguration in the range from 30 to 42%. In Ref. [36], a switchable band-stop filter with two different center frequencies is presented; the filter topology allows precise control over frequency and bandwidth discretely, achieved by choosing resonator sections switched by PIN diodes. In this article, we propose selectivity tuning in addition to center frequency and bandwidth reconfigurability, using a quasi-elliptic band-stop filter response; selectivity tuning is achieved in a band-stop filter response for the first time by the authors as compared to previous work in Ref. [1] and [11]. The filter is capable of having different center frequency states with precise bandwidth control, or alternatively having different fractional bandwidths with a fixed center frequency. Moreover, the filter allows selectivity tuning for each state. All filter parameters are tuned continuously. In this article, a discussion on the filter topology and its implementation, including a thorough comparison between simulations and measurements on filter performance, has been added compared to previous work [37].

2. BAND-STOP FILTER TOPOLOGY

The filter is designed to have a quasi-elliptic band-stop filter response, having one pair of transmission zeros at finite frequencies. The transmission zeros have been achieved by a cross-coupling line between a pair of nonadjacent resonators. The simulated [38] comparison between a Chebyshev and quasi-elliptic filter response is shown in Figure 1. The Chebyshev response is achieved by having a filter topology without the crossing line between nonadjacent resonators [1]. The quasi-elliptic band-stop ladder network of the proposed filter is shown in Figure 2. By varying the coupling capacitances $C_i$ between the main transmission line and each resonator, the bandwidth of the filter can be modified. The series LC models transmission line resonators are loaded by capacitances $C_j$ on one end. By varying $C_j$, the filter center frequency can be modified. By varying the cross coupling capacitance between nonadjacent resonators, $C_{ij}$, the filter selectivity can be modified.

The design of the reconfigurable four pole quasi-elliptic band-stop filter starts with a low-pass quasi-elliptic prototype filter [39]. It is well known that an element transformation of the low-pass element values and frequency mapping produces the desired theoretical filter response [39]. Through this method the values of $C_i$, $L$, and $C_{resonator}$ can be determined, where $C_{resonator} = (C \times C_j)/(C + C_j)$ (Fig. 2). The filter was initially designed to have a center frequency at 1.41 GHz, with a 15% fractional bandwidth. Varactor diodes were then inserted on the filter topology to tune filter parameters. The quasi-elliptic low-pass prototype elements used for the proposed filter are given in Table 1.

In Table 1, $Q_0$ is the attenuation pole frequency, $g_1$ and $g_2$ are the low-pass elements for the first and second resonator of the filter. Only half of the values are given due to filter symmetry. An immittance inverter $J_d$ is located between resonators 2 and 3 (Fig. 2). The immittance inverter $J_d$ corresponds to the

Figure 1 Comparison between a Chebyshev and a quasi-elliptic band-stop filter response. [Color figure can be viewed in the online issue, which is available at www.interscience.wiley.com]
cross coupling between nonadjacent resonators 1 and 4 (Fig. 2); \( J_a \) and \( J_b \) have opposite signs, and hence, the cross coupling line has an electrical length of 270°. From Table 1, the impedance of the line between resonators 2 and 3 in Figure 2 is around 50 \( \Omega \). Similarly, the impedance of the crossing line between resonators 1 and 4 in Figure 2 is found to be \( Z_0 = 250 \Omega \). As a 250 \( \Omega \) line is impractical for fabrication using conventional photolithography, two \( \theta/2 \) line sections connected by a series capacitor \( C_k \), as shown in Figure 2 were used instead. To find the value of capacitor \( C_k \), first the line length \( \theta \) is chosen to give a value of admittance \( Y_b \) from (1) that corresponds to a 65 \( \Omega \) impedance line suitable for realization using a conventional photolithographic process. Then, the value of \( C_k \) can be calculated using (2), where \( Z_0 \) is the characteristic impedance of the line and \( f_0 \) is the filter center frequency. Using (2), \( C_k \) was calculated to be 0.3pF, where \( \theta \) in (1) was taken as 120°.

\[
Y_b = \frac{J_b}{\tan \left( \frac{\theta}{2} \right)}
\]  
\[
C_k = \frac{J_b}{2\pi f_0 Z_0 \left[ 1 - \left( \frac{f_0}{f_c} \right)^2 \right]}
\]

3. FILTER IMPLEMENTATION

This section describes filter layout implementation, including bias circuitry, details of the surface mount components used, and substrate characteristics. Figure 3 shows the proposed filter topology layout that consists of a main transmission line with four transmission line resonators loaded with varactor diodes \( C_j \) on one end; these transmission line resonators correspond to the LC resonator model in Figure 2. Filter center frequency control can be attained by modifying the capacitance value of \( C_j \). These resonators are coupled to the main transmission line through varactor diode \( C_j \) destined to control the fractional bandwidth of the filter. Selectivity tuning was achieved by varactor diode \( C_k \) in the middle of the crossing line that produces the cross coupling between nonadjacent resonators (Figs. 2 and 3). Changing the capacitance of \( C_k \) varies the electrical length of the line and hence, the position of the transmission zeros located around the stop-band filter.

The filter was defined photolithographically on a Rogers Duroid substrate \((\varepsilon_r = 2.2\) and \( \tan \delta = 0.0009\)). The RF choke consists of a 177 nF inductor from Tyco Electronics with a self-resonance at 1.7 GHz. The choke inductor has been modeled as a 177 nF inductor in parallel with a 50 \( \Omega \) capacitor. Although the initial filter center frequency is around 1.4 GHz, the inductor isolation is better than −35 dB within the filter operation frequency range. Due to the high isolation of the choke inductor, the microwave signal is not influenced by the DC ports used to provide bias to the active devices.

To prevent a short circuit between the main transmission line and the crossing line, a DC block has been inserted on the crossing line, as shown in Figure 3. The DC block used was a 1 nF capacitor.

Three DC sources in Figure 3 were used to tune the three filter design parameters. \( V_i \) controls filter selectivity, \( V_j \) controls filter bandwidth, and \( V_k \) controls filter center frequency. These DC sources supply the reverse voltage needed to bias the varactor diodes. The grey zone in Figure 3 has positive polarization, and the black one has negative polarization. The varactors were biased using a voltage ranging from 0 to 20 V according to the manufacturer’s data sheet.

MACOM varactor diodes MA46470-276 were used for \( C_i \) and \( C_k \), and MA4ST402-287 were used for \( C_j \). The MA46470-276 diodes have a capacitance range from 0.3 to 1.8 pF, and the MA4ST402-287 diodes have a capacitance range from 3.86 to 86.29 pF. A photograph of the fabricated filter is shown in Figure 4.

4. RESULTS

This section contains simulated and measured results obtained from the proposed filter. The section is divided into three parts; Section 4.1 shows the results for center frequency tuning. Section 4.2 describes the results for bandwidth tuning. Finally, Section 4.3 shows the results obtained when selectivity is tuned. Simulated results were obtained using commercial software [38] to do electromagnetic simulations taking into account the surface mount component’s lumped element models. All measurements were done after a short-open-load-thru calibration, setting the measurement reference plane at the SMA coaxial connectors used to interface the filter with the measurement equipment. Scattering parameters were measured using an Agilent 8510C network analyzer, DC bias was supplied by two Promax FAC-662B power supplies.
4.1. Center Frequency Tuning

In simulations [38], \( C_k \) was fixed to 0.35 pF and \( C_i \) was fixed to 1.1 pF to tune filter center frequency. To tune filter center frequency, the capacitances of varactor diodes \( C_j \) were varied from 5.6 to 8.1 pF. A center frequency tuning of 60 MHz was achieved within this capacitance range, as shown in the upper part of Figure 5.

To obtain the filter tunable center frequency, the reverse voltage \( V_k \) was fixed to 15 V, \( V_i \) was fixed to 2 V, and \( V_j \) varied between 16 and 21 V. The varactor diodes \( C_j \) were reverse-biased by \( V_j \), which adjusts the resonator electrical lengths to produce the reconfigurable filter center frequency as shown in the lower part of Figure 5. The center frequency is tuned from 1.37 to 1.43 GHz using bias voltages from 16 to 21 V, respectively. A comparison between simulated and measured responses of the filter when tuning filter center frequency is shown in Table 2, where a very good agreement between simulations and measurements was obtained.

4.2. Bandwidth Tuning

In simulations [38], bandwidth tuning was controlled by \( C_i \), which modifies the resonator capacitive coupling from the main transmission line. This produces an additional variation of filter center frequency, due to the fact that varying \( C_i \) will slightly capacitively load the resonators. Therefore, \( C_j \) was slightly modified to readjust the electrical length of the resonators to provide a fixed filter center frequency at 1.46 GHz, while filter bandwidth was tuned. In simulations, \( C_i \) varied between 0.8 and 1.1 pF to reach a fractional bandwidth range from 9.46 to 12.93%, \( C_k \) was fixed to 0.35 pF for all states. The simulated response for the filter is shown in the upper part of Figure 6.

In the measurement setup, \( V_i \) and \( V_j \) were varied simultaneously, whereas \( V_k \) was set to 15 V. \( V_i \) sets the capacitance of \( C_i \) using a reverse voltage ranging from 2 to 5 V. Simultaneously, the DC source \( V_j \) adjusts the capacitance of \( C_j \) using a reverse voltage ranging from 14.1 to 20 V to maintain a fixed filter center frequency. The lower part of Figure 6 shows the measured response for the filter when bandwidth is tuned. The tuneable fractional bandwidth obtained for the filter ranges from 11.31 to 15.93%.

A comparison between simulations and measurements for filter bandwidth tuning is given in Table 3, where a very good agreement between simulations and measurements was obtained.

4.3. Selectivity Tuning

The simulated [38] response when tuning selectivity for the filter is shown in the upper part of Figure 7. First, bandwidth and center frequency are fixed, maintaining \( C_i = 1.1 \) pF and \( C_j = 5.8 \) pF and then different values of selectivity are taken by varying the capacitance of varactor diode \( C_k \) from 0.3 to 0.6 pF. Selectivity tuning is independent from the other design parameters. To calculate the selectivity for each filter state, we have taken the slope at the most linear part of the filter response in the passband to stopband transition region. The most linear region

<table>
<thead>
<tr>
<th>Table 2</th>
<th>Comparison Between Simulation and Measurement Results for Filter Centre Frequency Tuning</th>
</tr>
</thead>
<tbody>
<tr>
<td>State</td>
<td>( f_0(\text{GHz}) )</td>
</tr>
<tr>
<td>(a)</td>
<td>1.42</td>
</tr>
<tr>
<td>(b)</td>
<td>1.45</td>
</tr>
<tr>
<td>(c)</td>
<td>1.46</td>
</tr>
<tr>
<td>(d)</td>
<td>1.48</td>
</tr>
<tr>
<td>( f_0 ) range tuning: 60 MHz</td>
<td>( f_0 ) range tuning: 60 MHz</td>
</tr>
</tbody>
</table>
was found to be between −5 and −10 dB. A selectivity tuning range from 0.53 to 0.65 dB/MHz has been obtained.

In the lower part of Figure 7, filter selectivity tuning measurements are shown. The most linear part of the filter response in the passband to stopband transition was chosen to calculate the slope and found to be between −5 and −10 dB. Varactor diode $C_k$ is reverse-biased by $V_k$ to produce a selectivity variation from 0.37 to 0.40 dB/MHz, whereas $V_i$ and $V_j$ are fixed to 2 and 20 V, respectively. The reverse voltage of $V_k$ ranges from 2 to 20 V. A higher variation in selectivity was observed when the bias voltage values were in the range from 15 to 20 V. Within these voltage bias values, the capacitance of the varactor diode is smaller. Therefore, as the reverse voltage increases, the capacitance values decrease and filter selectivity becomes lower. Table 4 contains a comparison of simulated and measured results for selectivity tuning. The measured selectivity tuning range was limited by the parasitic capacitance of the varactor diode mount, including solder pads and solder that added up to the bias voltage values were in the range from 15 to 20 V. Within these voltage bias values, the capacitance of the varactor diode is smaller. Therefore, as the reverse voltage increases, the capacitance values decrease and filter selectivity becomes lower. Table 4 contains a comparison of simulated and measured results for selectivity tuning. The measured selectivity tuning range was limited by the parasitic capacitance of the varactor diode mount, including solder pads and solder that added up to the varactor diode $C_k$ capacitance; this displaced the overall capacitance value outside the values that would give a maximum selectivity tuning. Although selectivity tuning has been successfully demonstrated, higher values of measured selectivity tuning can be obtained by accurately modeling the varactor diode mount to account for capacitive parasitic effects that can affect the overall value of $C_k$ calculated from (2).

5. CONCLUSIONS

A reconfigurable band-stop filter with a pair of transmission zeros has been proposed and successfully demonstrated. A careful comparison between simulations and measurements has been carried out to validate the design theory and implementation. Selectivity tuning for the filter has been obtained by varying the capacitance of a varactor diode situated on the crossing line that produces the cross-coupled circuit. Bandwidth tuning has been controlled by varactor diodes used to couple resonators to the main transmission line. The center frequency of the filter is controlled by varactor diodes placed at the end of transmission line resonators. The filter topology presented in this article is able to tune all filter design parameters continuously and can be perfectly adjusted to produce a fractional bandwidth range from 11.51 to 15.46%, a center frequency range from 1.34 to 1.42 GHz, and a selectivity tuning range from 0.37 to 0.40 dB/MHz.

**TABLE 3** Comparison between Simulation and Measurement Results for Filter Bandwidth Tuning

<table>
<thead>
<tr>
<th>State</th>
<th>FBW(%)</th>
<th>$f_0$(GHz)</th>
<th>FBW(%)</th>
<th>$f_0$(GHz)</th>
</tr>
</thead>
<tbody>
<tr>
<td>(a)</td>
<td>12.96</td>
<td>1.47</td>
<td>15.93</td>
<td>1.43</td>
</tr>
<tr>
<td>(b)</td>
<td>11.86</td>
<td>1.46</td>
<td>13.29</td>
<td>1.42</td>
</tr>
<tr>
<td>(c)</td>
<td>10.65</td>
<td>1.46</td>
<td>11.97</td>
<td>1.42</td>
</tr>
<tr>
<td>(d)</td>
<td>9.46</td>
<td>1.46</td>
<td>11.31</td>
<td>1.42</td>
</tr>
<tr>
<td>Total fractional bandwidth variation: 3.50%</td>
<td>Total fractional bandwidth variation: 4.62%</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**TABLE 4** Comparison between Simulation and Measurement Result for Filter Selectivity Tuning

<table>
<thead>
<tr>
<th>State</th>
<th>Simulation</th>
<th>Measurement</th>
</tr>
</thead>
<tbody>
<tr>
<td>(a)</td>
<td>0.65</td>
<td>0.40</td>
</tr>
<tr>
<td>(b)</td>
<td>0.64</td>
<td>0.39</td>
</tr>
<tr>
<td>(c)</td>
<td>0.63</td>
<td>0.38</td>
</tr>
<tr>
<td>(d)</td>
<td>0.58</td>
<td>0.38</td>
</tr>
<tr>
<td>(e)</td>
<td>0.53</td>
<td>0.37</td>
</tr>
</tbody>
</table>

Selectivity tuning range: 0.12 dB/MHz

**ACKNOWLEDGMENTS**

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**REFERENCES**

ABSTRACT: A novel wideband cross-coupled band-pass filter based on defected stepped impedance resonator (DSIR) is presented in this article. Although the DSIR has opposite impedance characteristic and field distribution to the microstrip SIR, its resonant property is similar to the latter. The internal coupling coefficients of DSIRs are found to be large enough for the wideband filter design. A four-pole, cross-coupled band-pass filter with $f_0 = 1.6$ GHz and FBW = 12% is designed and fabricated using the folded DSIR. Experimental result has good agreement with the simulation. © 2010 Wiley Periodicals, Inc.

Key words: defected stepped impedance resonator (DSIR); wideband; cross-coupled; band-pass filter (BPF)

1. INTRODUCTION

Recent development in wireless communication system has created a need of band-pass filters with low insertion loss as well as high out-of-band rejection. Cross-coupled filters are widely investigated because they introduce one or more additional couplings between nonadjacent resonators and create finite transmission zeros out of the passband [1, 2]. Furthermore, high data-